

Faculty Position Department of Electronic Engineering UTFSM

Nov. 07, 2023

Job post Title: Faculty Position at the Department of Electronic Engineering, Universidad Técnica Federico Santa María.

Position: Faculty Member

Country: Chile

City: Valparaíso

Employer Name: Universidad Técnica Federico Santa María (UTFSM)

Department: Department of Electronic Engineering

Job description: This call is aimed at filling one full-time faculty positions for the Department of Electronic Engineering at Universidad Técnica Federico Santa María (UTFSM or USM, for short) in the field of Computer Engineering (Code 23-01-126).

Candidates may be Chilean or foreign nationals, in possession of a doctoral degree, or in the final stages of receiving it, with specialization in one of the following areas: processors and specialized computing systems, embedded systems, computer architecture for parallel processing, operating systems, or other subdisciplines related to the integration of software, firmware and hardware.

The selected candidates are expected to be competent and highly motivated academics who will teach in undergraduate and graduate (Master and Doctorate) programs, do research, and participate in innovation and administration activities. The willingness of the candidates to perform theoretical and experimental teaching, and previous experience in basic and applied research, is particularly encouraged.

Benefits: UTFSM offers an attractive starting salary, depending on initial academic evaluation. Additional benefits include free lunch, supplemental health insurance and incentives for publications in Web of Science indexed journals. Moreover, the salary can be further improved if the candidate is granted a research project, funded by the national agency for research and development (ANID), which gives monthly monetary incentives.

Job Responsibilities: The successful candidate is expected to contribute to teaching in undergraduate and graduate (Master and Doctorate) programs and to exhibit research and innovation capabilities. An interim performance agreement will be established for a period of one, two, or three years depending on the candidate's academic experience, establishing teaching and research goals to be achieved in that time frame. If necessary, the interim agreement will also provide a Spanish language training program to achieve language proficiency for teaching purposes. Fulfilling this agreement goals will enable the candidate to participate on tenure track.

Desired skills and experience:

1. The trajectory and expertise of the candidate must be consistent with the teaching and research duties in the advertised fields.
2. To have teaching abilities and to be willing to teach at undergraduate and graduate levels (theoretical and experimental).

3. To have enabling academic training to teach some of the following subjects: digital systems, computer architecture, microcontroller programming, data structure and algorithms, operating systems, system programming, computer networks, integrated circuit design.
4. To be willing to participate in training programs aimed at the use of modern educational methodologies and in the planning of the engineering education curriculum.
5. Exhibit demonstrable results in the creation of knowledge through recent publications in WoS indexed journals, international conferences, or patents.
6. Experience in applying to competitive research funds is desirable.
7. Writing and speaking proficiency in English and basic Spanish. Applicants not fluent in Spanish should show willingness to acquire within two-year sufficient proficiency to be able to teach and interact with students.
8. Willingness to carry out innovation, technology transfer, and industrial consulting projects.

Documentation required to apply.

1. Curriculum Vitae.
2. Formulation of a personal proposal for academic development in research and teaching.
3. Summary of your research career, referencing up to five of your most notable publications.
4. Undergraduate and graduate grade transcripts.
5. Certificates of diplomas and degrees (the selected candidate will have to present the original documents or certified copies).
6. Contact information (postal and email) of two senior academics who are willing to provide academic and personal references. One of the referees should be the Ph.D. thesis advisor. These references will be requested directly by the Head of the Department of Electronic Engineering, UTFSM.

Selection Procedure

A department level committee will review and possibly interview the candidates and then present a shortlist of selected candidates to the Department of Electronic Engineering for its approval. At the university level, another Selection Committee will interview all candidates in the shortlist. They should present a public lecture on a topic chosen in agreement between the applicant and the Department. Each interview and lecture will be via videoconference if a visit to UTFSM cannot be arranged. The Selection Committee will then rank these candidates, for the University authority to make the final decision.

Submission of Applications and Important Dates

Applications should be sent in electronic format to: postulaciones.ddrrhhad@usm.cl, please send a copy to: concursos.elo@usm.cl. The subject of the message should include the position code: 23-01-126.

Application Deadline: December 31st, 2023

The selected candidate could fill the position starting July 1st, 2023, or earlier depending on availability.

Fields of expertise: Electronic Engineering, Electrical Engineering

About the employer: The Universidad Técnica Federico Santa María (www.usm.cl) is one of the oldest and traditional public universities in Chile, with more than 91 years of history. Since its foundation, the University has been focused on the development of Science, Engineering and Technology. Our university has a strong emphasis on research and on scientific and technological education. USM is ranked first place nationally in Engineering and technology (US News Education-Best Global Universities by Subject 2021: Engineering, ARWU Subject 2021: Electrical & Electronic Engineering and Shanghai Ranking 2021 Ing. Elec & Electr). In 2022 is ranked among the top three engineering schools of the country in Ing. Elec & Electr, Physics and Mathematical Sciences.

The Electronic Engineering Department of UTFSM is located at the main Campuses of the Universidad Técnica Federico Santa María, namely the Valparaíso and Santiago Campuses. Valparaíso is a major city, seaport, and university center as well as an important touristic attraction, being declared a UNESCO World Heritage Site and Chile's cultural capital. It is located at a 120 km distance from Santiago, the capital of Chile where the San Joaquín Campus is located. According to the United Nations Development Programme, Chile is one of South America's most stable and prosperous nations, leading Latin American nations in human development, competitiveness, income per capita, globalization and economic freedom. Since July 2013, Chile is considered by the World Bank as a "high-income economy", and hence as a developed country.